

Patent #: 6,900,654

9/26/2005

DOCKET NO.: L/M-102-DIV



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,900,654

Date of Patent: May 31, 2005

Serial No.: 09/832,884

Confirmation No.: 2718

Divisional of US Patent 6,221,682 (09/321,565)

Applicant: Steve M. Danziger, et al.

Art Unit: 2829

Filed: April 12, 2001

Examiner: Evan T. Pert

Docket No: L/M-102-DIV

Customer No: 28892

For: Method and Apparatus for Evaluating a Known Good Die Using
Both Wire Bond and Flip-Chip Interconnects

REQUEST FOR CORRECTION UNDER 37 CFR § 1.323

US Patent & Trademark Office

Customer Service Window, Mail Stop: **CERTIFICATE OF CORRECTION**

Randolph Building

401 Dulany Street

Alexandria, VA 22314

Sir:

Applicant hereby requests the Commissioner to correct the name of the Assignee (including city and state) of this patent.

The correct, complete name of the Assignee is: **BAE Systems Information and Electronic Systems Integration Inc. of Nashua, NH (US)**.

This patent is a divisional of US Patent 6,221,682 (issued April 24, 2001). The full name of the Assignee of US Patent 6,221,682 was correctly recorded by the Patent Office on June 22, 2001 at Reel 012287, Frame 0780 (see the bottom of page 3 of the attached Notice of Recordation of Assignment Document dated November 7, 2002).

This error was a clerical mistake made on the Fee Transmittal Form (PTOL-85) filed on September 21, 2004 and was made without

09/27/2005 JADD01 00000119 6900654 100.00 OP
02 FC:1811

Patent #: 6,900,654

9/26/2005

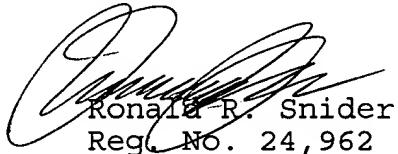
DOCKET NO.: L/M-102-DIV

deceptive intent. It is, therefore, respectfully requested that the Certificate of Correction be issued.

Applicant submits with this request a photocopy of the Notice of Recordation of Assignment Document (November 7, 2002), Recordation Form Cover Sheet and Assignment, two copies of PTO/SB/44 (formerly PTO-1050) are attached and the fee of \$100.00 (Fee Code 1811) required by 37 CFR § 1.20(a).

Still further, if there is any error in the fee, the Commissioner is authorized to charged the undersigned's Deposit Account No. 19-2816. A duplicate copy of this paper is attached.

Respectfully submitted,



Ronald R. Snider
Reg. No. 24,962

September 26, 2005

Snider & Associates
Ronald R. Snider
P.O. Box 27613
Washington, D.C. 20038-7613

RRS/bam

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 6,900,654

APPLICATION NO.: 09/832,884

ISSUE DATE : May 31, 2005

INVENTOR(S) : Steve M Danziger and Tushar Shah

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page

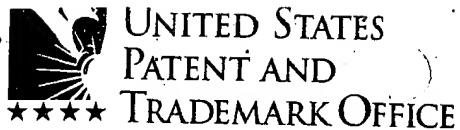
Item (73), Assignee, delete "BAE Systems - Information & Electronic Warfare Systems, Manassas, VA" and substitute therefore --- BAE Systems Information and Electronic Systems Integration Inc., Nashua, NH---

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Ronald R. Snider
P.O. Box 27613
Washington, DC 20038-7613

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



CORRECTED
NOTICE

NOVEMBER 07, 2002

SWIDLER BERLIN SHEREFF FRIEDMAN, LLP
EDWARD A. PENNINGTON
3000 K STREET, N.W.
SUITE 300
WASHINGTON, D.C. 20007-5116

RECEIVED

PTAS NOV 18 2002

SWIDLER BERLIN SHEREFF FRIEDMAN LLP

Under Secretary of Commerce For Intellectual Property and
Director of the United States Patent and Trademark Office
Washington, DC 20231
www.uspto.gov

102270601A

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF
THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS
AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER
REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE
INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA
PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD
FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY
CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723.
PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE,
ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY,
SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 06/22/2001

REEL/FRAME: 012287/0780
NUMBER OF PAGES: 31

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

LOCKHEED MARTIN CORPORATION

DOC DATE: 11/27/2000

ASSIGNEE:

BAE SYSTEMS INFORMATION AND
ELECTRONIC SYSTEMS INTEGRATION,
INC.
P.O. BOX 868, NHQ1-719
NASHUA, NEW HAMPSHIRE 03061

SERIAL NUMBER: 06393012
PATENT NUMBER:

FILING DATE: 06/28/1982
ISSUE DATE:

SERIAL NUMBER: 06643902
PATENT NUMBER:

FILING DATE: 07/06/1984
ISSUE DATE:

SERIAL NUMBER: 07246136
PATENT NUMBER:

FILING DATE: 09/14/1988
ISSUE DATE:

SERIAL NUMBER: 07544140
PATENT NUMBER:

FILING DATE: 06/13/1990
ISSUE DATE:

DOCKETED

KRM 1-2127/03

SERIAL NUMBER: 08141505
PATENT NUMBER:

FILING DATE: 10/26/1993
ISSUE DATE:

SERIAL NUMBER: 09384429
PATENT NUMBER:

FILING DATE: 08/27/1999
ISSUE DATE:

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ISSUE DATE: 02/13/2001

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PATENT NUMBER: 6351451

FILING DATE: 07/25/2000
ISSUE DATE: 02/26/2002

SERIAL NUMBER: 09512671
PATENT NUMBER:

FILING DATE: 02/24/2000
ISSUE DATE:

SERIAL NUMBER: 94208079
PATENT NUMBER: 4208079

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ISSUE DATE:

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FILING DATE: 09/20/1983
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ISSUE DATE: 01/10/1989

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PATENT NUMBER: 4879675

FILING DATE: 02/17/1988
ISSUE DATE: 11/07/1989

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PATENT NUMBER: 4852060

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ISSUE DATE: 07/25/1989

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ISSUE DATE:

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ISSUE DATE:

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ISSUE DATE:

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PATENT NUMBER:

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ISSUE DATE:

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ISSUE DATE: 09/10/2002

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ISSUE DATE: 08/27/2002

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012287/0780 PAGE 7

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PATENT NUMBER: 5504703

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ISSUE DATE: 04/02/1996

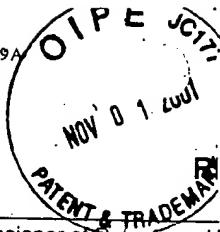
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PATENT NUMBER: 5365469

FILING DATE: 01/13/1993
ISSUE DATE: 11/15/1994

DOROTHY BASMA, PARALEGAL
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS



11-05-2002

U.S. Department of Commerce
Patent and Trademark Office
PATENT

102270601

11-101

TO: The Commissioner of Patents and Trademarks: Please record the attached original document(s) or copy(ies).

Submission Type

New
 Resubmission (Non-Recordation)

Document ID # 101791706

Correction of PTO Error

Reel # [] Frame # []

Corrective Document

Reel # [] Frame # []

Conveyance Type

Assignment
 License
 Merger

Security Agreement

Change of Name

Other []

U.S. Government

(For Use ONLY by U.S. Government Agencies)

Departmental File Secret File

Conveying Party(ies)

Mark if additional names of conveying parties attached

Name (line 1) LOCKHEED MARTIN CORPORATION

Execution Date
Month Day Year
11 27 2000

Name (line 2) []

Second Party

Name (line 1) []

Execution Date
Month Day Year
[]

Name (line 2) []

Receiving Party

Mark if additional names of receiving parties attached

Name (line 1) BAE SYSTEMS INFORMATION AND ELECTRONIC

If document to be recorded
is an assignment and the
receiving party is not
domiciled in the United States,
an appointment of a domestic
representative is attached.
(Designation must be a
separate document from
Assignment.)

Name (line 2) SYSTEMS INTEGRATION, INC.

Address (line 1) P.O. Box 868, NHQ1-719

Address (line 2) []

Address (line 3) Nashua

NH

03061-0868

City

State/Country

Zip Code

Domestic Representative Name and Address

Enter for the first Receiving Party only.

Name []

Address (line 1) []

Address (line 2) []

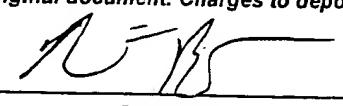
Address (line 3) []

Address (line 4) []

FOR OFFICE USE ONLY

Public burden reporting for this collection of information is estimated to average approximately 30 minutes per Cover Sheet to be recorded, including time for reviewing the document and gathering the data needed to complete the Cover Sheet. Send comments regarding this burden estimate to the U.S. Patent and Trademark Office, Chief Information Officer, Washington, D.C. 20231 and to the Office of Information and Regulatory Affairs, Office of Management and Budget, Paperwork Reduction Project (0651-0027), Washington, D.C. 20503. See OMB Information Collection Budget Package 0651-0027, Patent and Trademark Assignment Practice. DO NOT SEND REQUESTS TO RECORD ASSIGNMENT DOCUMENTS TO THIS ADDRESS.

Mail documents to be recorded with required cover sheet(s) information to:
Commissioner of Patents and Trademarks, Box Assignments, Washington, D.C. 20231

Correspondent Name and Address		Area Code and Telephone Number	202 424-7500		
Name	Edward A. Pennington				
Address (line 1)	Swidler Berlin Shereff Friedman, LLP				
Address (line 2)	3000 K Street, N.W.				
Address (line 3)	Suite 300				
Address (line 4)	Washington, D.C. 20007-5116				
Pages	Enter the total number of pages of the attached conveyance document including any attachments # 24				
Application Number(s) or Patent Number(s)		<input checked="" type="checkbox"/> Mark if additional numbers attached.			
Enter either the Patent Application Number or the Patent Number (DO NOT ENTER BOTH numbers for the same property).					
Patent Application Number(s)		Patent Number(s)			
06/393,012	06/643,902	07/246,136	4,208,079	4,725,986	4,797,804
07/544,140	08/141,505	09/384,429	4,879,675	4,852,060	4,996,670
08/884,675	09/625,641	09/512,671	5,117,129	5,234,851	5,307,466
If this document is being filed together with a new Patent Application, enter the date the patent application was signed by the first named executing inventor.			Month	Day	Year
Patent Cooperation Treaty (PCT)					
Enter PCT application number <u>only if a U.S. Application Number as not been assigned.</u>	PCT	PCT	PCT	PCT	PCT
Number of Properties	Enter the total number of properties involved. # 97				
Fee Amount	Fee Amount for Properties Listed (37 CFR 3.41): \$ 40.00				
Method of Payment: Deposit Account	<input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Deposit Account				
Enter for payment by deposit account or if additional fees can be charged to the account.)					
Deposit Account Number: # 19-5127, Order #19944.0001					
Authorization to charge additional fees: Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>					
Statement and Signature					
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Charges to deposit account are authorized, as indicated herein.					
Robert C. Bertin, Reg. 41,488				November 1, 2001	
Name of Person Signing	Signature			Date	



Attorney Ref: 19944.0001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner of Patents and Trademarks
Box Assignments
Washington, D.C. 20231

REQUEST FOR CORRECTION OF RECORDATION

Dear Sir:

Enclosed please find a copy of:

- (1) A Corrected Recordation Form Cover Sheet and accompanying materials filed on November 1, 2001; and
- (2) A Notice of Recordation dated January 11, 2002 accepting the document for recording.

The Notice of Recordation is incorrect in that it lists only the 18 patent applications and patents that were listed on the Corrected Recordation Form Cover Sheet. The Notice does not include the other 79 of the 97 total properties identified in the Corrected Recordation Form Coversheet and the attached Annex. The applicants request appropriate correction.

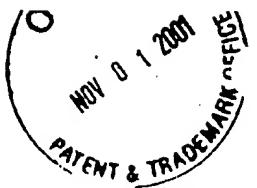
If the Examiner has any questions, he is encouraged to call the undersigned attorney.

If any further fees are due in order to complete the recording process, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 19-5127, Docket 19944.0001.

Respectfully submitted,

Robert C. Bertin
Registration No. 41,488
SWIDLER BERLIN SHEREFF FRIEDMAN, LLP
3000 K Street, N.W., Suite 300
Washington, D.C. 20007
(202) 424-7500

Date: October 31, 2002



Attorney Ref: 19944.0001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner of Patents and Trademarks
Box Assignments
Washington, D.C. 20231

CORRECTED RECORDATION FORM COVER SHEET PURSUANT TO 37 C.F.R. §3.34

Dear Sir:

In response to the attached communication received from the U.S. Patent and Trademark Office, dated October 15, 2001, in which the attached recordation form cover sheet was found non-recordable because of the format associated with numbers identifying applications and patents therein, the applicants herewith submit:

- (1) A copy of the Notice of Non-Recordation of Document;
- (2) A copy of the Recordation Form Cover Sheet, accompanying papers found Non-Recordable;
- (3) A copy of the November 27, 2001 Assignment document for recording; and
- (4) A new Recordation Form Cover Sheet.

The new Recordation Form Cover Sheet includes both the series code and application number for patent applications, omits extraneous information found in the previous Recordation Form Cover Sheet and groups the applications and patents as requested.

The assignee requests that the recordation date for this corrected filing be set at June 22, 2001 pursuant to 37 C.F.R. §3.51 as the assignee is making the instant filing prior to the November 15, 2001 deadline set in the notice of non-recordation.

The undersigned has authorized the charging of its deposit account in the attached recordation form cover sheet. If any further fees are due in order to complete the recording process, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No.19-5127, Docket 19944.0001.

Respectfully submitted,



Robert C. Bertin
Registration No. 41,488

Date: November 1, 2001

Attorney Ref: 19944.0001

SWIDLER BERLIN SHEREFF FRIEDMAN, LLP
3000 K Street, N.W., Suite 300
Washington, D.C. 20007
(202) 424-7500

**NOV. 27, 2000 PATENT AND PATENT APPLICATION CONVEYANCE
- ANNEX TO RECORDATION FORM COVER SHEET -**

FROM: LOCKHEED MARTIN CORPORATION
**TO: BAE SYSTEMS INFORMATION AND ELECTRONIC
SYSTEMS INTEGRATION, INC.**

ADDITIONAL PATENT NUMBERS FOR RECORDING

5,365,469	5,527,724	5,314,841	5,644,286
5,360,752	5,448,572	5,358,879	5,457,591
5,504,703	5,504,793	5,525,923	5,598,041
5,915,082	6,065,135	5,793,836	5,858,817
5,796,274	5,807,649	5,901,148	6,034,399
D414,486	5,970,095	5,867,479	6,091,704
6,111,780	5,756,234	6,052,606	6,127,879

ADDITIONAL PATENT APPLICATION NUMBERS FOR RECORDING

09/007,980	09/570,050	08/987,016	09/566,178
09/570,064	09/320,227	09/320,207	09/491,230
09/502,062	09/480,454	09/441,941	09/559,661
09/559,660	09/307,126	09/321,565	09/449,723
09/563,197	09/598,681	09/591,958	09/553,595
09/591,731	09/589,732	09/624,247	09/559,659
09/597,229	09/590,805	09/592,403	09/590,806
09/590,809	09/630,216	09/651,156	09/667,040
09/636,125	09/441,942	09/651,155	
60/142,035	60/178,247	60/163,757	60/146,895
60/224,649	60/200,348	60/224,650	60/207,913
60/176,606	60/224,648	60/181,512	60/201,122
60/203,895	60/220,700	60/139,897	60/120,328



FILING RECEIPT

PLEASE DATE STAMP AND RETURN TO US - BOX 34385

In re application of:

LOCKHEED MARTIN CORPORATION

Appl. No. Various

Confirmation No. (Unassigned)

Filed: N/A

For: VARIOUS APPLICATIONS

Art Unit: N/A

Examiner: N/A

Atty. Docket No.: 36994-163241

Customer No.:



26694

PATENT TRADEMARK OFFICE

PAPER(S) FILED ENTITLED:

1. Recordation of Assignments
(for 37 issued patents and 49 pending applications)
2. Fee Transmittal Sheet

VENABLE
1201 New York Avenue, N.W.
Suite 1000
Washington, D.C. 20005-3917
Telephone: (202) 962-4800
Facsimile: (202) 962-8300

DOCKET NO.: 36994-163241
ATTORNEY/SEC: RPA/lpm

Date Filed: June 22, 2001

Venable Filing No. 97763 (late filing)

FEE TRANSMITTAL for FY 2001

Patent fees are subject to annual revision.

TOTAL AMOUNT OF PAYMENT (\$ 3,440)

Complete If Known	
Application Number	See Attached Lists for Patents and Pending Applications
Filing Date	N/A
First Named Inventor	N/A
Examiner Name	N/A
Group / Art Unit	N/A
Attorney Docket No.	N/A

O I P E JUN 22 2001
P A T E N T M A T T E R S C C

METHOD OF PAYMENT (check one)

1. The Commissioner is hereby authorized to charge indicated fees and credit any over payments to:

Deposit Account Number
12-2141

Deposit Account Name
BAE SYSTEMS

Charge Any Additional Fee Required
Under 37 CFR 1.16 and 1.17
 Applicant claims small entity status.
See 37 CFR 1.27

2. Payment Enclosed:

Check Credit card Money Order Other

FEE CALCULATION

1. BASIC FILING FEE

Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
101	710	201	355	Utility filing fee	0
106	320	206	160	Design filing fee	
107	490	207	245	Plant filing fee	
108	710	208	355	Reissue filing fee	
114	150	214	75	Provisional filing fee	
SUBTOTAL (1)				(S) 0	

2. EXTRA CLAIM FEES

Total Claims	Extra Claims	Fee from below	Fee Paid
Independent Claims	0 -20 = 0	X 18	= 0
Multiple Dependent	0 -3 = 0	X 80	= 0

Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
103	18	203	9	Claims in excess of 20	
102	80	202	40	Independent claims in excess of 3	
104	270	204	135	Multiple dependent claim, if not paid	
109	80	209	40	** Reissue independent claims over original patent	
110	18	210	9	** Reissue claims in excess of 20 and over original patent	
SUBTOTAL (2)				(S) 0	

** or number previously paid, if greater; For Reissues, see above

FEE CALCULATION (continued)						
3. ADDITIONAL FEES	Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee or oath		
127	50	227	25	Surcharge - late provisional filing fee or cover sheet		
139	130	139	130	Non-English specification		
147	2,520	147	2,520	For filing a request for reexamination		
112	920*	112	920*	Requesting publication of SIR prior to Examiner action		
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action		
115	110	215	55	Extension for reply within first month		
116	390	216	195	Extension for reply within second month		
117	890	217	445	Extension for reply within third month		
118	1,390	218	695	Extension for reply within fourth month		
128	1,890	228	945	Extension for reply within fifth month		
119	310	219	155	Notice of Appeal		
120	310	220	155	Filing a brief in support of an appeal		
121	270	221	135	Request for oral hearing		
138	1,510	138	1,510	Petition to institute a public use proceeding		
140	110	240	55	Petition to revive – unavoidable		
141	1,240	241	620	Petition to revive – unintentional		
142	1,240	242	620	Utility issue fee (or reissue)		
143	440	243	220	Design issue fee		
144	600	244	300	Plant issue fee		
122	130	122	130	Petitions to the Commissioner		
123	130	123	130	Petitions related to provisional applications		
126	180	126	180	Submission of Information Disclosure Stmt		
581	40	581	40	Recording each patent assignment per property (times number of properties)		3,440
146	710	246	355	Filing a submission after final rejection (37 CFR § 1.129(a))		
149	710	249	355	For each additional invention to be examined (37 CFR § 1.129(b))		
179	710	279	355	Request for Continued Examination (RCE)		
169	900	169	900	Request for expedited examination of a design application		
Other fee (specify) _____						
*Reduced by Basic Filing Fee Paid						SUBTOTAL (3)
						(\$ 3,440)

SUBMITTED BY

Complete (if applicable)					
Name (Print/Type)	Ralph P. Albrecht	Registration No. Attorney/Agent)	43,466	Telephone	(202) 216-8166
Signature				Date	June 22, 2001

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

FEE TRANSMITTAL for FY 2001

Patent fees are subject to annual revision.

TOTAL AMOUNT OF PAYMENT (\$ 3,440)

Complete If Known	
Application Number	See Attached Lists for Patents and Pending Applications
Filing Date	N/A
First Named Inventor	N/A
Examiner Name	N/A
Group / Art Unit	N/A
Attorney Docket No.	N/A

O P E R A T I O N S C E N T E R
P A T E N T & T R A D E M A R K O F F I C E

JUN 22 2001

METHOD OF PAYMENT (check one)

1. The Commissioner is hereby authorized to charge indicated fees and credit any over payments to:

Deposit Account Number

Deposit Account Name

Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17

Applicant claims small entity status. See 37 CFR 1.27

2. Payment Enclosed:

Check Credit card Money Order Other

FEE CALCULATION

1. BASIC FILING FEE

Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
101	710	201	355	Utility filing fee	0
106	320	206	160	Design filing fee	
107	490	207	245	Plant filing fee	
108	710	208	355	Reissue filing fee	
114	150	214	75	Provisional filing fee	

SUBTOTAL (1)

2. EXTRA CLAIM FEES

Total Claims	Extra Claims	Fee from below	Fee Paid
Independent Claims	0 -20 = 0	X 18 = 0	0

Multiple Dependent X =

Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
103	18	203	9	Claims in excess of 20	
102	80	202	40	Independent claims in excess of 3	
104	270	204	135	Multiple dependent claim, if not paid	
109	80	209	40	** Reissue independent claims over original patent	
110	18	210	9	** Reissue claims in excess of 20 and over original patent	

SUBTOTAL (2)

3. ADDITIONAL FEES	Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee or oath		
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112	920*	112	920*	Requesting publication of SIR prior to Examiner action		
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action		
115	110	215	55	Extension for reply within first month		
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117	890	217	445	Extension for reply within third month		
118	1,390	218	695	Extension for reply within fourth month		
128	1,890	228	945	Extension for reply within fifth month		
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121	270	221	135	Request for oral hearing		
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141	1,240	241	620	Petition to revive – unintentional		
142	1,240	242	620	Utility issue fee (or reissue)		
143	440	243	220	Design issue fee		
144	600	244	300	Plant issue fee		
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123	130	123	130	Petitions related to provisional applications		
126	180	126	180	Submission of Information Disclosure Stmt		
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179	710	279	355	Request for Continued Examination (RCE)		
169	900	169	900	Request for expedited examination of a design application		

Other fee (specify) _____

*Reduced by Basic Filing Fee Paid

SUBTOTAL (3)

SUBMITTED BY

Complete (if applicable)

Name (Print/Type)	Ralph P. Albrecht	Registration No. Attorney/Agent	43,466	Telephone	(202) 216-8166
Signature				Date	June 22, 2001

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

07-30-2001

Tab settings → → →

To the Honorable Commissioner

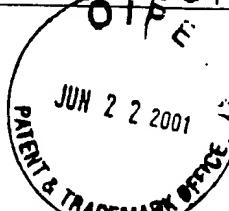
101791706

tached original documents or copy thereof.

1. Name of conveying party(ies):
LOCKHEED MARTIN CORPORATION
6801 Rockledge Drive
Bethesda, Maryland 20817

101-22-01

Additional name of conveying party(ies) attached? Yes No



3. Nature of conveyance:

Assignment Merger
 Security Agreement Change of Name
 Other _____

Execution Date: November 27, 2000

2. Name and address of receiving party(ies)

Name: BAE SYSTEMS INFORMATION AND

Internal Address: ELECTRONIC SYSTEMS INTEGRATION,
INC.

Street Address: 9300 Wellington Road

City: Manassas _____ State: VA _____ Zip: 20110-4122

Additional Name(s) & address(es) attached? Yes No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s)

Attached List of 37 Issued Patents

B. Patent No.(s)

Attached List of 49 Pending Applications

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning this document should be mailed:



26694

PATENT TRADEMARK OFFICE

Name: VENABLE

Address: P.O. Box 34385

City: Washington State: D.C. Zip: 20043-9998

6. Total number of applications and patents involved: 86

7. Total fee (37 CFR 3.41) \$ 3,440.00

Enclosed

Authorized to be charged to deposit account

8. Deposit account number:

12-2141 BAE SYSTEMS

(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

RALPH P. ALBRECHT

Name of Person Signing

Signature

June 22, 2001

Date

Total number of pages including cover sheet, attachments, and documents: 28

Mail documents to be recorded with required cover sheet information to:

Commissioner of Patents & Trademarks, Box Assignments
Washington, D.C. 20231

VENABLE

ATTORNEYS AT LAW

27/2001 JJALLAH2 00000015 122141 4208079

C:581 3440.00 CH

ISSUED PATENTS - (Total 35)

	<u>CASE NO.</u>	<u>ATTORNEY DOCKET NO.</u>	<u>PATENT NO.</u>	<u>ISSUED DATE</u>
1	FE-00003		4,208,079	17-Jun-80
2	FE-00007		4,725,986	16-Feb-88
3	FE-00009		4,797,804	10-Jan-89
4	FE-00010		4,879,675	07-Nov-89
5	FE-00012		4,852,060	25-Jul-89
8	FE-00017		4,996,670	26-Feb-91
7	FE-00021		5,117,129	26-May-92
8	FE-00029		5,234,851	10-Aug-93
9	FE-00031		5,307,466	26-Apr-94
10	FE-00035		5,365,469	15-Nov-1994
11	FE-00041		5,527,724	18-Jun-1996
12	FE-00042		5,314,841	24-May-1994
13	FE-00052		5,644,286	01-Jul-1997
14	FE-00060		5,360,752	01-Nov-1994
15	FE-00069		5,448,572	05-Sep-1995
16	FE-00070		5,358,879	25-Oct-1994
17	FE-00071		5,457,591	10-Oct-1995
18	FE-00072		5,504,703	02-Apr-1996
19	FE-00073		5,504,793	02-Apr-1996
20	FE-00074		5,525,923	11-Jun-1996
21	FE-00090		5,598,041	28-Jan-1997
22	FE-00101	36996-163317	5,915,082 6,065,135	22-Jun-1999 16-May-2000
23	FE-00107		5,793,836	11-Aug-98
24	FE-00113		5,858,817	12-Jan-99
25	FE-00114		5,796,274	18-Aug-98
26	FE-00115		5,807,649	15-Sep-98
27	FE-00121		5,901,148	04-May-99
28	FE-00122	36994-163227	6,034,399	07-Mar-00
29	FE-00124		D414,486	28-Sep-99
30	FE-00126		5,970,095	19-Oct-99
31	FE-00131	36994-163234 36994-163258	5,867,479 6,091,704	02-Feb-99 18-Jul-00
32	FE-00165	36996-163342	6,111,780	29-Aug-00
33	FE-00172		5,756,234	26-May-98
34	FE-00233	36994-163247	6,052,606	18-Apr-2000
35	FE-00291		6,127,879	03-Oct-2000

PENDING APPLICATIONS - (Total 49)

	<u>CASE NO.</u>	<u>ATTORNEY DOCKET NO.</u>	<u>APPLICATION NO.</u>	<u>ISSUED DATE</u>
1	FE-00006		393,012 - Secret	28-Jun-1982
2	FE-00016		246,136 - Secret	13-Sep-1988
3	FE-00020		544,140 - Secret	13-Jun-1990
4	FE-00057		141,505	26-Oct-1993
5	FE-00124-1		384,429	27-Aug-1999
6	FE-00130	36994-165241	08/884,675 - Now as: Patent No. 6,188,874	27-Jun-1997
7	FE-00131 (FE-00131.2)	36994-165441	09/625,641	25-Jul-2000
9	FE-00170		512,671	30-Jun-2000
9	FE-00185	36996-163289	09/007,980	16-Jan-1998
10	FE-00221	36994-163257	09/570,050	12-May-2000
11	FE-00258	36994-163244	08/987,016	09-Dec-1997
12	FE-00275		566,178	05-May-2000
13	FE-00276	36762-162147	09/570,064	12-May-2000
14	FE-00277		320,227	26-May-1999
15	FE-00278		320,207	26-May-1999
16	FE-00295		491,230	25-Jan-2000
17	FE-00300	36996-163297	09/502,062	10-Feb-2000
18	FE-00320	36762-162136	09/480,454	11-Jan-2000
19	FE-00321		441,941	17-Nov-1999
20	FE-00352	36996-163322	09/559,661	28-Apr-2000
21	FE-00354	36966-163323	09/559,660	28-Apr-2000
22	FE-00371		307,126	07-May-1999
23	FE-00372		321,565	28-May-1999
24	FE-00375		449,723	24-Nov-1999
25	FE-00385		563,197	02-May-2000
26	FE-00387		598,681	21-Jun-2000
27	FE-00397		591,958	12-Jun-2000
28	FE-00398		60/178,247	21-Jan-2000
29	FE-00414		553,595	20-Apr-2000
30	FE-00422		591,731	12-Jun-2000
31	FE-00426		60/163,757	05-Nov-1999
32	FE-00431		624,247	24-Jul-2000
33	FE-00432	36996-163338	09/559,659	28-Apr-2000
34	FE-00434		597,229	20-Jun-2000
35	FE-00439		590,805	09-Jun-2000

PENDING APPLICATIONS - (Total 49)

	<u>CASE NO.</u>	<u>ATTORNEY DOCKET NO.</u>	<u>APPLICATION NO.</u>	<u>ISSUED DATE</u>
36	FE-00443		590,806	09-Jun-2000
37	FE-00444		590,809	09-Jun-2000
38	FE-00450		60/224,649	11-Aug-2000
39	FE-00456		630,216	01-Aug-2000
40	FE-00458		651,156	30-Aug-2000
41	FE-00458	36762-162139	60/200,348	28-Apr-2000
42	FE-00464		667,040	21-Sep-2000
43	FE-00469		636,125	10-Aug-2000
44	FE-00481		60/224,648	11-Aug-2000
45	FE-00483	36762-162143	60/181,512	11-Feb-2000
46	FE-00486		60/201,122	02-May-2000
47	FE-00492		60/203,895	12-May-2000
48	FE-00498		60/209,665	06-Jun-2000
49	FE-00522		60/220,700	25-Jul-2000

PATENT ASSIGNMENT

THIS PATENT ASSIGNMENT is made and entered into as of the 27th day of November 2000, between Lockheed Martin Corporation, a Maryland corporation ("Assignor"), and BAE SYSTEMS INFORMATION AND ELECTRONIC SYSTEMS INTEGRATION INC. (formerly known as BAE SYSTEMS Sanders Inc.), a Delaware corporation ("Assignee").

WITNESSETH:

WHEREAS, Assignor is the owner of the entire right, title and interest in and to all of the patents and patent applications set forth on Schedule A annexed hereto and made a part hereof and has the unrestricted right to sell, assign and transfer such patents and patent applications; and

WHEREAS, pursuant to the terms of a Transaction Agreement, dated as of July 13, 2000, by and among Assignor, Assignee and BAE SYSTEMS North America Inc., a Delaware corporation, Assignor has agreed, among other things, to transfer to Assignee said patents and patent applications;

NOW, THEREFORE, in consideration of the sum of ten (\$10.00) dollars and other good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which is hereby acknowledged, Assignor hereby sells, assigns, transfers and sets over to Assignee, its successors and permitted assigns, Assignor's entire right, title and interest in and to the patents and patent applications set forth on Schedule A hereto, including (without limitation) all divisions, reissues, substitutions, continuations and extensions thereof, all priority rights under the International Convention for the Protection of Industrial Property for every member country (and any other international convention or treaty), any and all Letters Patent and reissues and extensions of Letters Patent granted thereon and any and all rights corresponding to any of the foregoing throughout the world and any and all accounts, contract rights, warranties, litigation claims and rights, including the right to sue for and collect upon all claims for profits and damages as a result of past infringement, if any, and other general intangibles of Assignor related to any of the foregoing, in each case whether now existing or hereafter acquired or created, whether owned, leased or licensed beneficially or of record and whether owned, leased or licensed individually, jointly or otherwise, together with the products and proceeds thereof (including license royalties and the proceeds of infringement suits), all payments and other distributions with respect thereto and any divisions, reissues, substitutions, continuations and extensions of any and all of the foregoing (all of the foregoing herein collectively referred to as the "Patents").

Assignor further agrees that it shall on the date hereof and from time to time thereafter, at the request of Assignee, perform or cause to be performed such acts and execute, acknowledge and deliver at the request of Assignee, such documents as may reasonably be required to evidence or effectuate the sale, conveyance, assignment, transfer and delivery to Assignee of the Patents or for the performance by Assignor of any of its obligations hereunder.

IN WITNESS WHEREOF, Assignor has executed this Patent Assignment as of the date above written.

LOCKHEED MARTIN CORPORATION

By:

Warren W. Lanning

Title: Director, Business Ventures

BAE SYSTEMS INFORMATION AND
ELECTRONIC SYSTEMS INTEGRATION INC.

By:

Earl D. Morris

Title: Vice President

DISTRICT OF COLUMBIA ss.:

On the 27th day of November, 2000, before me personally came Warren W. Lanning, to me known (or satisfactorily proven), who being by me duly sworn, did depose and say that he is the Director, Business Ventures of Lockheed Martin Corporation, the corporation described in, and which executed the foregoing instrument, and that he was fully authorized to execute this Patent Assignment on behalf of said corporation.

Lis A. Young
Notary Public
My Commission Expires June 2002

DISTRICT OF COLUMBIA ss.:

On the 27th day of November, 2000, before me personally came Eric D. Munns, to me known (or satisfactorily proven), who being by me duly sworn, did depose and say that he is the Vice President and Secretary of BAE SYSTEMS Information and Electronic Systems Integration Inc., the corporation described in, and which executed the foregoing instrument, and that he was fully authorized to execute this Patent Assignment on behalf of said corporation.

Lis A. Young
Notary Public

SCHEDULE A

LM Space Electronics & Communications

CaseNumber	Title	Country	Status	File Date	ApplNumb	Issue Date	PatNumber
FE-000003	High Density Integrated Circuit	US	Expired	24-Jan-1979	5946	17-Jun-1980	4,200,079
FE-000006	Process for Increasing the Immunity of IC to Ionizing Radiation	JP	Abandoned	20-Dec-1979	16407G779	20-Jan-1984	1185552
FE-000007	EEPROM Read Only Memory Cell With Work Line Augmented Precharging Of Bit Lines	US	Secret	28-Jun-1982	393,012		
		JP	Abandoned	15-May-1984	095787704	10-Nov-1988	1464935
		GB	Granted	24-Jul-1984	84108711.7	27-Sep-1989	0135699
		FR	Granted	24-Jul-1984	84108711.7	27-Sep-1989	0135699
		AT	Granted	24-Jul-1984	084108711.7	27-Sep-1989	0135699
		US	Granted	20-Sep-1983	534,035	16-Feb-1988	4,725,906
		DE	Granted	24-Jul-1984	084108711.7	27-Sep-1989	0135699
		EP	Granted	24-Jul-1984	84108711.7	27-Sep-1989	0135699
FE-000008	Improved Local Oxide Isolation Process	US	Secret	06-Jul-1984	643,902		

CaseNumber	Title	Country	Status	File Date	Appl/Numb	Issue Date	PatNumber
FE-00009	High Density, High Performance, Single Event Upset Immune Data	US	Granted	09-Mar-1987	23,426	10-Jan-1989	4,797,804
		FR	Granted	22-Jan-1988	88100947.6	13-Jan-1993	281,741
		JP	Granted	20-Jan-1988	008597788	07-Oct-1994	1878682
		EP	Granted	22-Jan-1988	88100947.6	13-Jan-1993	281,741
		GB	Granted	22-Jan-1988	88100947.6	13-Jan-1993	281,741
		DE	Granted	22-Jan-1988	88100947.6	13-Jan-1993	3877381308
FE-00010	Purity Generator Circuit And Method						

FE-00010 Purity Generator Circuit And Method

FE-00012 Soft Error Resistant CMOS Datta Cells

FE-0016 Process for Increasing the Immunity of LC to Ionizing Radiation

Tuesday, November 21, 2000

CaseNumber Title
FE-00017 Zero Standby Power, Radiation Hardened, Memory Redundancy Circuit

Country	Status	File Date	Appl/Numb	Issue Date	Pat/Number
<i>Redundancy</i>					
EP	Granted	03-May-1990	90108357.6	21-Aug-1996	419,760
DE	Granted	03-May-1990	90108357.6	21-Aug-1996	69028169.2
JP	Granted	07-Sep-1990	236022790	17-Dec-1999	3015084
US	Granted	28-Sep-1999	414,809	26-Feb-1991	4,996,670
GB	Granted	03-May-1990	90108357.6	21-Aug-1996	419,760
FR	Granted	03-May-1990	90108357.6	21-Aug-1996	419,760
WO	Pending	09-Sep-1990	236022790		419,760

FE-00020 A Method for improving gate oxide reliability of SOI in Mesa Type Transistor.

FE-00021 CMOS Off Chip Driver for Fault Tolerant Cold Splicing

Country	Status	File Date	Appl/Numb	Issue Date	Pat/Number
<i>Cold Splicing</i>					
EP	Granted	08-Oct-1991	91117104.9	28-Feb-1996	481,329
JP	Granted	11-Oct-1991	290475/91	08-Aug-1996	2549221
DE	Granted	08-Oct-1991	91117104.9	28-Feb-1996	69117420.2
FR	Granted	08-Oct-1991	91117104.9	28-Feb-1996	481,329
US	Granted	16-Oct-1990	598,300	26-May-1992	5,117,129
GB	Granted	08-Oct-1991	91117104.9	28-Feb-1996	481,329

FE-00022 Method of Forming A frontside Contact to the Silicon Substrate of a SOI Wafer.

Country	Status	File Date	Appl/Numb	Issue Date	Pat/Number
<i>SOI Wafer</i>					
JP	Granted	28-Mar-1994	057624/94	02-Dec-1996	2509952

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CaseNumber	Title	Country	Status	File Date	Appl/Numb	Issue Date	PatNumber
FE-00031	Distributed Programmable Priority Arbitration	JP	Granted	01-Mar-1993	39810/93	27-Jun-1996	2531918
FE-00035	Fast Fourier Transform Using Balanced Coefficients	US	Granted	30-Apr-1992	076,239	26-Apr-1994	5,307,466
FE-00040	An Electrostatic Discharge Protect Diode For Silicon-On-Insulator Technology	JP	Granted	22-Oct-1991	301336/91	13-Jun-1997	2662124
		EP	Pending	13-Jun-1991	91109688.1		
		US	Granted	13-Jan-1993	004,217	15-Nov-1994	5,365,469
FE-00041	Method To Prevent Latch-Up And Improve Breakdown Voltage In SOI Mosfets	JP	Granted	28-Mar-1994	6,57668/94	09-May-1997	2647339
		DE	Granted	17-Feb-1994	94102415.0	21-Apr-1999	0622850
		FR	Granted	17-Feb-1994	94102415.0	21-Apr-1999	0622850
		EP	Granted	17-Feb-1994	94102415.0	21-Apr-1999	0622850
		GB	Granted	17-Feb-1994	94102415.0	21-Apr-1999	0622850
		US	Abandoned	30-Apr-1993	056,042		0622850
		EP	Pending	17-Feb-1994	94102414.3		
		US	Granted	12-Sep-1994	304,639	18-Jun-1996	5,527,724

CaseNumber Title

CaseNumber	Title	Country	Status	File Date	Appl/Numb	Issue Date	PatNumber
FE-00042	A Method Of Forming A Frontside Contact To The Silicon Substrate Of A SOI Wafer	US	Granted	30-Apr-1993	054,992	24-May-1994	5,314,841
		DE	Pending	17-Feb-1994	94102416.0		
		EP	Pending	17-Feb-1994	94102416.8		
		JP	Granted	18-Apr-1994	070654/94	26-Feb-1999	2891321
		GB	Pending	17-Feb-1994	94102416.8		
		FR	Pending	17-Feb-1994	94102416.8		

Power Bus Digital Communication System

FE-00057	<i>Single Even Upset Hardening Of Commercial VLSI Technology Without Circuit Redesign</i>	US	Granted	04-Oct-1993	131,346	01-Jul-1997	5,644,286
		US	Pending	26-Oct-1993	141,505		
		JP	Abandon	19-Oct-1994	6-253600		
		EP	Pending	30-Aug-1994	94113502.2		
FE-00060	<i>Method To Radiation Harden The Buried Oxide In Silicon On-Insulator Structures</i>	US	Granted	28-Oct-1993	142,030	01-Nov-1994	5,360,752
		EP	Granted	14-Oct-1994	94116233.1	22-Dec-1999	65259 A1
FE-00069	<i>Spare Signal Line Switching Method and Apparatus</i>	US	Granted	14-Mar-1994	212,372	05-Sep-1995	5,448,572
		JP	Granted	26-Jun-1992	168028/92	21-Nov-1996	2113094

CaseNumber	Title	Country	Status	File Date	App/Numb	Issue Date	PatNumber
FE-000070	Gate Overlapped Lightly Doped Drain For Buried Channel Devices	GB	Granted	26-May-1994	94108054.1	29-Nov-1995	0684640
		US	Granted	30-Apr-1993	054,994	25-Oct-1994	5,358,879
		FR	Granted	26-May-1994	94108054.1	29-Nov-1995	684640
		JP	Granted	04-Jun-1994	6-127422	19-Sep-1997	2698046
		DE	Granted	26-May-1994	94108054.1	29-Nov-1995	0684640
		EP	Granted	26-May-1994	94108054.1	10-Nov-1999	0684640
FE-000071	Current Overload Protection Circuit	US	Granted	12-Jan-1995	371,718	10-Oct-1995	5,457,591
FE-000072	Single Event Upset Hardened CMOS Latch Circuit	US	Granted				
FE-000073	Magnification Correction For Ion-Alpha Proximity X-Ray Lithography	US	Granted	01-Feb-1995	302,112	02-Apr-1996	5,504,703
FE-000074	Single Event Upset Immune Register With Fast Write Access	US	Granted	17-Feb-1995	309,993	02-Apr-1996	5,504,793
		US	Granted	21-Feb-1995	391,798	11-Jun-1996	5,525,923

CaseNumber	Title	Country	Status	File Date	ApplNumb	Issue Date	PatNumber
FE-00075	Checkpoint Retry Mechanism	US	Abandoned	23-Aug-1988	205,345	27-Mar-1990	4,912,707
		JP	Abandoned	11-Aug-1989	0207115	07-Apr-1995	1922412
		GB	Abandoned	08-Jun-1989	89110329.3	13-Aug-1995	0355286
		DE	Abandoned	08-Jun-1989	89110329.3	13-Aug-1995	0355286
		FR	Abandoned	08-Jun-1989	89110329.3	13-Aug-1995	0355286
		EP	Abandoned	08-Jun-1989	89110329.3	13-Aug-1995	0355286
FE-00090	Efficient Dual Source Fault Tolerant Power Controller	US	Granted	16-Nov-1995	559,504	28-Jan-1997	5,598,041
FE-00091	A Scalable, Radiation Hardened Shallow trench Isolation	US	Abandoned	01-Jul-1999	60/142,035		
		US	Pending	30-Jun-2000			
FE-00092	Apparatus And Method For Cooling Standard Electronic Modules	US	Abandoned	19-Jan-1996	588,804		
FE-00101	Error Detection and Fault Isolation For Lockstep Processor Systems	US	Granted	07-Jun-1996	660,640	22-Jun-1999	5,915,002
		US	Granted	04-Jun-1999	325,641	16-May-2000	6,065,135
FE-00107	X-Ray Mask Pellicle	US	Granted	06-Sep-1996	716,657	11-Aug-1998	5,793,036

CaseNumber	Title	Country	Status	File Date	ApplNumb	Issue Date	PatNumber
FE-00113	Process To Personalize Master Slice-Wafer And Fabricate High Density VLSI Components With A Single Masking Step	US	Granted	10-Oct-1996	728,880	12-Jan-1999	5,850,817
FE-00114	Fault Tolerant MOSFET Driver	US	Granted	16-Oct-1996	733,000	18-Aug-1998	5,796,274
FE-00115	Lithographic Patterning Method And Mask Set Therefor With Light Field Trim Mask	US	Granted	31-Oct-1996	740,590	15-Sep-1998	5,007,649
FE-00121	Ring Domains For Bandwidth Sharing	US	Granted	06-Mar-1997	812,104	04-May-1999	5,901,140
FE-00122	Electrostatic Discharge Protection For Silicon-On-Insulation	US	Granted	06-Mar-1997	812,103	07-Mar-2000	6,034,399
FE-00124	Dual Mode Telephone Handset Satellite Telephone Handset -	US	Granted	27-Mar-1997	29069642	28-Sep-1999	D414,486
FE-00124-1	Satellite Telephone Handset	US	Pending	27-Aug-1999	384,429		
FE-00126	Secure Data Transmission on A TDMA Synchronous Network	US	Granted	14-Apr-1997	837,165	19-Oct-1999	5,970,095

CaseNumber	Title	Country	Status	File Date	Appl/Numb	Issue Date	PatNumber
<i>FE-00130 Improved Control Telemetry Signal Communication System For Geostationary Satellites</i>							
		US	Pending	27-Jun-1997	084,675		
		US	Pending	23-Dec-1999	084,675		
FE-00131	Digital Multi-Channel Demultiplexer/Multiplexer (MCD/M) Architecture	US	Pending	25-Jul-2000	625,641		
		US	Granted	27-Jun-1997	084,650	02-Feb-1999	5,867,479
		US	Granted	01-Feb-1999	241,313	16-Jul-2000	6,091,704
FE-00144	Dual Mode Collision Avoidance System	US	Abandon				
FE-00165	Radiation Hardened Six Transistor Random Access Memory and Memory Device	US	Abandon	04-Jun-1998	090,946		
		US	Granted	05-Jun-1999	325,645	29-Aug-2000	6,111,780
FE-00170	Enhanced TRENCH Isolation (STI) Method for Fabricating Radiation-Tolerant Integrated Circuit Devices	WO	Pending	04-Jun-1999	US99/12442		
FE-00172	High accuracy Fabrication Of X-Ray Masks With Optical And E-Beam Lithography	US	Pending	30-Jun-2000	512,671		
FE-00185	Integrated Circuit Package and Method Increasing Density Of I/O Leads	US	Granted	04-Jun-1996	603,826	26-May-1998	5,756,234
		US	Pending	16-Jan-1998	007,980		

<i>CaseNumber</i>	<i>Title</i>	<i>Country</i>	<i>Status</i>	<i>File Date</i>	<i>ApplNumb</i>	<i>Issue Date</i>	<i>PatNumber</i>
<i>FE-00221</i>	<i>Distributed Determination of Explicit Rate in an ATM Communication System</i>	US	Expired	03-Jun-1999	60/137,595		
		US	Expired	19-Oct-1999	60/160,302		
<i>FE-00228</i>	<i>Elimination of Narrow Device Width Effects In Complementary Metal Oxide Semiconductor (CMOS) Devices</i>	US	Pending	12-May-2000	570,050		
		US	Unfilled				
<i>FE-00233</i>	<i>Reversible Keypad And Display For A Telephone Handset</i>	US	Granted	12-Dec-1997	989,463	18-Apr-2000	6,052,606
		US	Granted				
<i>FE-00239</i>	<i>Recessed Gate Process For Silicon-On-Insulator Devices</i>	US	Abandoned	04-Nov-1997	964,022		
		US	Abandoned				
<i>FE-00258</i>	<i>Shallow Isolation Trench Forming Process For Silicon-on-Insulator Technology</i>	US	Pending	09-Dec-1997	987,016		
		US	Pending				
<i>FE-00275</i>	<i>Cold Spare and Voltage Interoperable Off-Chip Driver and Associated Methods</i>	US	Pending	05-May-2000	566,178		
		US	Expired	02-Jun-1999	60/137,174		
<i>FE-00276</i>	<i>Self Equalized Low Power Precharge Sense AMP For High Speed Memory</i>	US	Pending	12-May-2000	570,064		
		US	Expired	10-Feb-2000	60/181,559		
		US	Expired	01-Jun-1999	60/137,224		

CaseNumber	Title	Country	Status	File Date	Appl/Number	Issue Date	Pat/Number
FE-00277	A Memory Device Having Reduced Power Requirements And Associated Methods	US	Pending	26-May-1999	320,227		
FE-00278	A Memory Device Having A Chip Select Speedup Feature and Associated Methods	US	Pending	26-May-1999	320,207		
FE-00279	Single Event Upset Hardened CMOS Latch Circuit With Fast Write Time	US	Abandoned	07-Oct-1998	168,430		
FE-00291	Multi-Channel Overvoltage Protection Circuit	US	Granted	26-Feb-1998	030,902	03-Oct-2000	6,127,079
FE-00295	Method For Fabricating Resistors Within Semiconductor Integrated Circuit Devices	US	Pending	25-Jan-2000	491,230		
		US	Expired	01-Feb-1999	60110,049		
FE-00300	In Situ Proximity Gap Monitor For Lithography	US	Pending	10-Feb-2000	502,062		
		US	Expired	24-Sep-1999	60155,571		
FE-00319	Enhanced Local Oxidation of Silicon (LOCOS) Method for Fabricating Radiation-Tolerant Integrated Circuit Devices	US	Unfiled				

CaseNumber	Title
FE-00320	Enhanced Single Event Upset Immune Latch Circuit

Country	Status	File Date	Appl/Number	Issue Date	Pat/Number
US	Pending	11-Jan-2000	400,454		
WO	Pending	11-Jan-2000	US00/00557		
US	Expired	28-Jul-1999	60/145,939		
<i>Method and Apparatus for Hardening A Static Random Access Memory Cell From Single Event Upsets</i>					
US	Pending	17-Nov-1999	441,941		
DE	Pending	17-Nov-1999	US99/27302		
GB	Pending	17-Nov-1999	US99/27302		
FR	Pending	17-Nov-1999	US99/27302		
EP	Pending	17-Nov-1999	US99/27302		
US	Abandon	28-May-1999	60/136,480		
<i>Method For Improving Radiation Tolerance of Semiconductor Integrated Circuit Devices</i>					
US	Pending	22-Jun-2000			
US	Expired	22-Jun-1999	60/139,097		
<i>Method and Apparatus for a SEU Tolerant Clock Splitter</i>					
US	Expired	30-Apr-1999	60/131,926		
US	Pending	28-Apr-2000	559,661		
<i>Method and Apparatus for A Scannable Hybrid Flip Flop</i>					
US	Pending	20-Apr-2000	559,660		
WO	Pending	28-Apr-2000	US00/11340		
US	Expired	30-Apr-1999	60/132,121		

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<i>CaseNumber</i>	<i>Title</i>	<i>Country</i>	<i>Status</i>	<i>File Date</i>	<i>ApplNumb</i>	<i>Issue Date</i>	<i>PatNumber</i>
FE-00371	<i>Pattern Density Tailoring For Litching of Advanced Lithographic Masks</i>	US	Pending	07-May-1999	307,126		
FE-00372	<i>Method and Apparatus For Evaluating A Known Good Die Using Both Wire Bond and Flip-Chip Interconnects</i>	US	Pending	28-May-1999	321,565		
FE-00375	<i>Single-Event Upset Hardened Reconfiguration Bi-Stable CMOS Latch</i>	WO	Pending	20-Jan-2000	US00/01356		
		US	Pending	24-Nov-1999	449,723		
		US	Abandon	23-Dec-1999	60171,589		
FE-00385	<i>Method and Apparatus for a Voltage Responsive RESET for EEPROM</i>	US	Pending	02-May-2000	563,197		
		US	Expired	02-Jun-1999	60/137,739		
FE-00387	<i>Low-Power CMOS Device and Logic Gate/Circuits Thereof</i>	WO	Pending	22-Jun-2000	US00/11887		
		US	Expired	09-Nov-1999	60/164,343		
		US	Expired	23-Jun-1999	60/140,361		
		US	Pending	21-Jun-2000	598,681		

<i>CaseNumber</i>	<i>Title</i>	<i>Country</i>	<i>Status</i>	<i>File Date</i>	<i>ApplNumb</i>	<i>Issue Date</i>	<i>PatNumber</i>
FE-00391	Single Event Upset (SEU) Hardened Static Random Access Memory Cell	US	Expired	28-May-1999	601136,479		
		US	Pending	17-Nov-1999	441,942		
		US	Pending	30-Aug-2000	651,155		
FE-00397	Circuit and Method For Limiting Inrush Current Through A Mosfet	EP	Pending	17-Nov-1999	US99/27301		
	Circuit and Method For Limiting Inrush Current Through A Transistor/102	US	Expired	30-Dec-1999	601174,059		
		WO	Unified				
FE-00398	Integrated Resistor Having Aligned Body and Contact and Method for Forming The Same	US	Pending	12-Jun-2000	591,958		
FE-00406	Storage Unit Subassembly Insertion/Extraction Tool	US	Provision	21-Jan-2000	601178,247		
FE-00410	Computer Device Having Multiple Linked Parallel Buses and Associated Method	US	Expired	17-Feb-1999	601120,320		
FE-00414	Self-Restoring Single Event Upset (SEU) Hardened Multiport Memory Cell	US	Pending	14-Sep-2000			
		US	Expired	28-May-1999	601136,478		
		WO	Pending	15-May-2000	US00/13095		
		US	Pending	20-Apr-2000	553,595		

CaseNumber	Title	Country	Status	File Date	App/Num	Issue Date	PatNumber
FE-00422	<i>System and Method of Providing a Spread Spectrum Pulse Width Modulator Clock</i>	US	Expired	18-Oct-1999	60/159,974		
		WO	Pending	12-Jun-2000	13300020748		
FE-00424	<i>Multiplexor Having A Single Event Upset (SEU) Immune Data Keeper Circuit</i>	US	Pending	12-Jun-2000	591,731		
FE-00426	<i>Oscillator and Method For Generating A Frequency Within A Stable Frequency Range</i>	US	Pending	08-Jun-2000	509,732		
FE-00431	<i>Method For Testing Known Good Die</i>	US	Provision	05-Nov-1999	60/163,757		
FE-00432	<i>Method and Apparatus for a Single Event Upset (SEU) Tolerant Clock Splitter</i>	US	Pending	24-Jul-2000	624,247		
		US	Expired	25-Oct-1999	60/161,418		
FE-00434	<i>Tool Suite for the Rapid Development of Advanced Standard Cell Libraries</i>	US	Expired	30-Apr-1999	60/131,925		
		WO	Pending	28-Apr-2000	US00/11349		
		US	Pending	28-Apr-2000	559,659		
FE-00436	<i>A Process For Removing A Silicon-Containing Material Through Use Of A Byproduct Generated During Formation Of A Diffusion Barrier Layer</i>	US	Pending	20-Jun-2000	597,229		
		US	Unfilled				

CaseNumber	Title	Country	Status	File Date	ApplNumb	Issue Date	PatNumber
FE-00439	<i>Increasing The Susceptibility Of Integrated Circuits To Ionizing Radiation</i>	US	Expired	11-Jun-1999	60138,718		
		US	Pending	09-Jun-2000	590,005		
FE-00442	<i>Semiconductor Circuit Having Increased Susceptibility To Ionizing Radiation</i>	US	Pending	09-Jun-2000	592,4-3		
		US	Expired	11-Jun-1999	60138,720		
FE-00443	<i>Semiconductor Device And Circuit Having Low Tolerance To Ionizing Radiation</i>	US	Pending	09-Jun-2000	590,006		
		US	Expired	11-Jun-1999	60138,721		
FE-00444	<i>Apparatus And Method For Manufacturing A Semiconductor Circuit</i>	US	Pending	09-Jun-2000	590,809		
		US	Pending	31-Jul-2000			
FE-00449	<i>Method to Harden Shallow Trench Isolation Against Total Ionizing Dose Radiation</i>	US	Pending	31-Jul-2000			
		US	Expired	02-Aug-1999	60146,895		
FE-00450	<i>A New Radiation-Hardened Technique For Preventing Latches From Single Event Upsets</i>	US	Provisioned	11-Aug-2000	60124,649		
		US	Expired	20-Jul-1999	60144,731		
FE-00451	<i>Radiation Hardened High Speed Differential Driver</i>						

CaseNumber	Title	Country	Status	File Date	Appl/Numb	Issue Date	Pat/Number
FE-00453	Radiation Hardened High Speed Differential Receiver	US	Expired	20-Jul-1999	60/144,625		
FE-00456	Radiation Hardened Silicon-On-Insulator (SOI) Transistor Having A Body Contact	US	Pending	01-Aug-2000	630,216		
		US	Expired	23-Dec-1999	60/171,569		
FE-00458	Circuit For Filtering Single Event Effect (SEE) Induced Glitches	US	Pending	30-Aug-2000	651,156		
		US	Expired	07-Sep-1999	60/152,340		
FE-00462	Method and Apparatus Radiation Hardened Clock Splitter	WO	Pending	06-Sep-2000	US0024421		
FE-00464	Single Event Upset Immune Oscillator Circuit	US	Provision	28-Apr-2000	60/200,348		
FE-00469	Redundant Oscillator and Method For Generating A Regulated Signal	US	Pending	21-Sep-2000	667,040		
FE-00470	Controlled Hermetic Solder Scaling For Large Perimeter Components	US	Pending	10-Aug-2000	636,125		
		US	Abandoned	20-Jan-2000	60/177,234		
		US	Expired	18-Jan-2000	60/176,574		

CaseNumber	Title	Country	Status	File Date	App/Numb	Issue Date	PatNumber
FE-00491	Rad Hard Ring Oscillator	US	Unfiled				
FE-00492	Single Event Upset Immune Logic Family	US	Provision	12-May-2000	60/203,895		
FE-00498	A Sense Amp Scheme Hardened for Dynamic Single Event Upsets	US	Unfiled				
FE-00503	Remote Pictures	US	Provision	06-Jun-2000	60/209,665		
FE-00505	Reduced Stress Interface Column Grid Array	US	Unfiled				
FE-00508	Remote Sonar Buoys	US	Unfiled				
FE-00512	Method For Advanced Fill Pattern Creation	US	Unfiled				
FE-00513	Power Conservation Circuit Using The "Seebeck" Effect	US	Unfiled				

<i>CaseNumber</i>	<i>Title</i>	<i>Country</i>	<i>Status</i>	<i>File Date</i>	<i>Appl/Number</i>	<i>Issue Date</i>	<i>Pat/Number</i>
<i>FE-00515</i>	<i>Method Of Forming and Applications of Micropipes and Chalcogenide Waveguides</i>	US	Unfiled				
<i>FE-00516</i>	<i>Fault Isolation Test Methodology</i>	US	Unfiled				
<i>FE-00521</i>	<i>Novel High-Density High-Performance CMOS SRAM Cell Design</i>	US	Unfiled				
<i>FE-00522</i>	<i>Novel CMOS SRAM Cell Design with Prescribed Power-On Data State</i>	US	Unfiled				
<i>FE-00527</i>	<i>Solder Column Attach</i>	Pending	25-Jul-2000	60/220,700			
<i>FE-00527</i>	<i>On-Chip High Speed Termination</i>	Open					
<i>FE-00528</i>	<i>An Institute Radiation Action of Integrated Circuits</i>	Open					
<i>FE-00539</i>	<i>Visitor Information System (VIS)</i>	Open					
<i>FE-00540</i>	<i>Small Area Sidewall Raster Contact For Chalcogenide Memory Device</i>	Open					
<i>FE-00542</i>	<i>EGA to CGA Conversion Process</i>	Open					